

1K × 8-BIT STATIC RAM MK4801A(P/J/N)-1/2/3/4

FEATURES

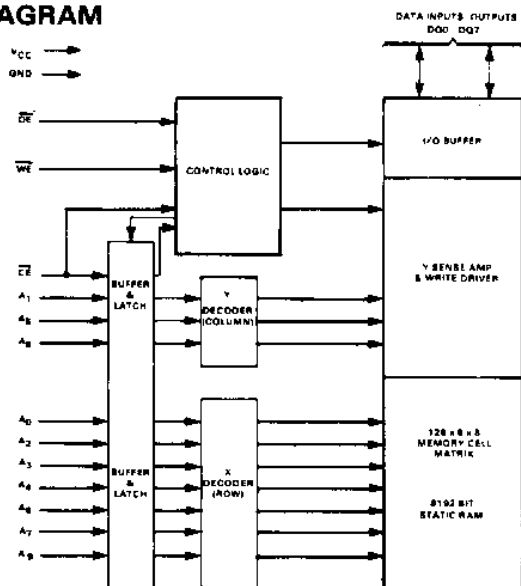
- Static operation
- Organization: 1K x 8 bit RAM JEDEC pinout
- High performance
- Pin compatible with Mostek's BYTEWYDE™ memory family
- 24/28 pin ROM/PROM compatible pin configuration
- CE and OE functions facilitate bus control

DESCRIPTION

The MK4801A uses Mostek's advanced circuit design techniques to package 8,192 bits of static RAM on a single chip. Static operation is achieved with high performance and low power dissipation by utilizing Address Activated™ circuit design techniques.

BLOCK DIAGRAM

Figure 1



TRUTH TABLE

CE	OE	WE	Mode	DQ
V _{IH}	X	X	Deselect	High Z
V _{IL}	X	V _{IL}	Write	D _{IN}
V _{IL}	V _{IL}	V _{IH}	Read	D _{OUT}
V _{IL}	V _{IH}	V _{IH}	Read	High Z

X = Don't Care

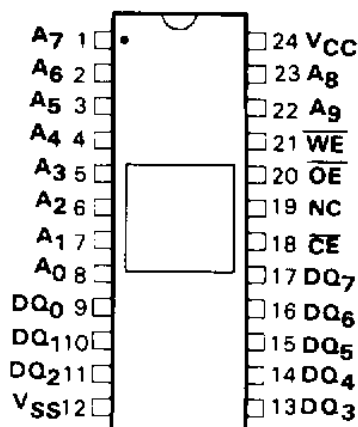
- MKB version screened to MIL-STD-883

Part No.	R/W Access Time	R/W Cycle Time
MK4801A-1	120 nsec	120 nsec
MK4801A-2	150 nsec	150 nsec
MK4801A-3	200 nsec	200 nsec
MK4801A-4	250 nsec	250 nsec

The MK4801A excels in high speed memory applications where the organization requires relatively shallow depth with a wide word format. The MK4801A presents to the user a high density cost effective N-MOS memory with the performance characteristics necessary for today's micro-processor applications.

PIN CONNECTIONS

Figure 2



PIN NAMES

A ₀ -A ₉	Address Inputs	WE	Write Enable
CE	Chip Enable	OE	Output Enable
V _{SS}	Ground	NC	No Connection
V _{CC}	Power (+5V)	DQ ₀ -DQ ₇	Data In/Data Out